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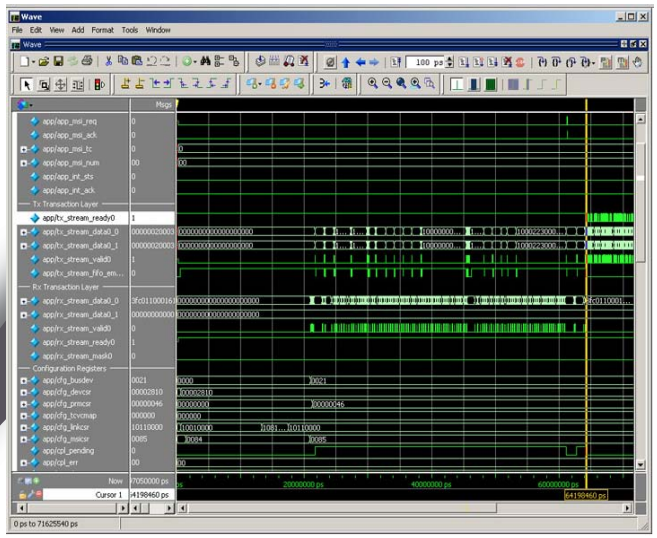
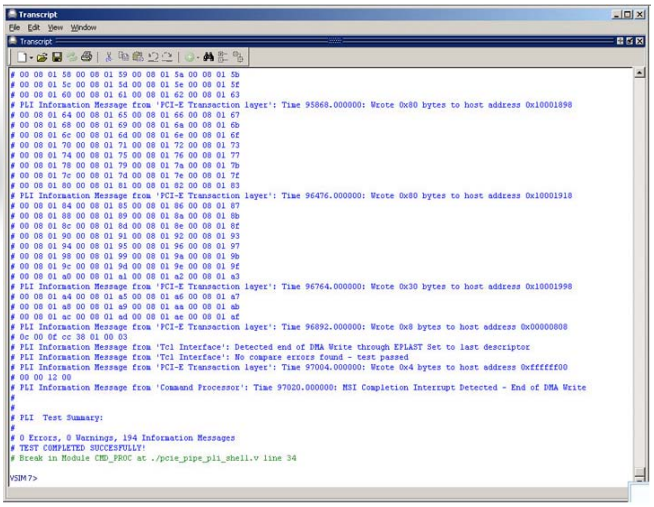
PCIe Driver Integration Tool

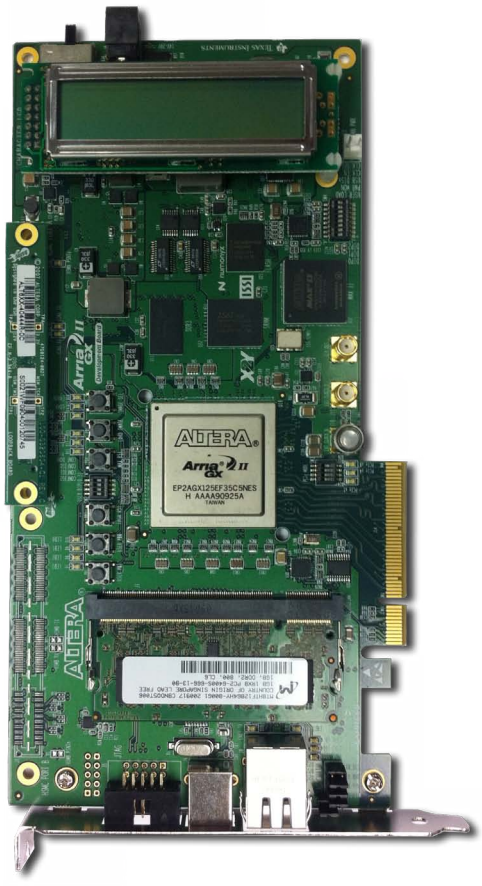
DrivExpress™ solves the problem of driver integration for PCIe implemented in FPGAs. PCIe is a very commonly used protocol, but implementing a custom driver-- needed for every FPGA design-- is a hard problem if all debug and testing can only be done in a FPGA HW. **DrivExpress™** provides a low cost and easy to use verification environment allowing PCIe driver and protocol issues to be tracked down and quickly resolved.





Features:

- PCIe Gen1, 2, and 3 capable (x1, x2, x4, with x16 capability)
- C++ PCIe Root Complex Model
- Allows Sophisticated PCIe System Simulations
- Python Script Driven
- Signficantly Faster than Verilog Based Test Benches
- Verbose Error Reporting, Flexible Event Programming for Rapid Debug
- Essential HW / SW Integration Tool for PCIe Driver Development



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DrivExpress™ PCIe *Development Flow*